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3	1170	hibernat$3	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/13 15:36
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6	11195050	@ad<20001227	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/13 15:37
7	13	(((power adj2 control$3) with (read or write)) same ((sleep adj mode) or hibernat$3)) and @ad<20001227	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/13 15:42
8	7450	wake-up or wake adj up	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/13 15:43
9	173	(wake-up or wake adj up) with wireless	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/13 15:43
10	0	((power adj2 control$3) with (read or write)) and ((wake-up or wake adj up) with wireless)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/13 15:43
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12	101	(wake-up or wake adj up) same (notebook or laptop or portable adj computer)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/13 15:44
13	39	(wake-up or wake adj up) with (notebook or laptop or portable adj computer)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/13 15:44
14	22	@ad<20001227 and ((wake-up or wake adj up) with (notebook or laptop or portable adj computer))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/13 16:05
15	277508	synchronize or synchronized or synchronization	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/13 16:06
16	4	(@ad<20001227 and ((wake-up or wake adj up) with (notebook or laptop or portable adj computer))) and (synchronize or synchronized or synchronization)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/13 16:24
17	1509	711/154.ccls.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/13 16:24

18	1267	713/300.ccls.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/13 16:24
19	301	713/310.ccls.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/13 16:25
20	494	713/324.ccls.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/13 16:25
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1 **Low current magnetic-RAM memory operation with a high sensitive spin valve material**
Matsuyama, K.; Asada, H.; Ikeda, S.; Taniguchi, K.;
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Chun-yen Chang;
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Zorpette, G.;
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5 **HeRMES: high-performance reliable MRAM-enabled storage**
Miller, E.L.; Brandt, S.A.; Long, D.D.E.;
Hot Topics in Operating Systems, 2001. Proceedings of the Eighth Workshop on , 20-22 May 2001

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10 Effects of pumping time on GMR and coercivity of RF-sputtered MRAM dual spin-valves
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[Abstract]   [PDF Full-Text (108 KB)]   IEEE JNL

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16 **A novel low power VMRAM/MTJ design with robust magnetic switching**
Xiaochun Zhu; Jian-Gang Zhu;
Magnetics Conference, 2003. INTERMAG 2003. IEEE International , 28 March-3 April 2003
Pages:ED - 07

[Abstract]   [PDF Full-Text (194 KB)]   **IEEE CNF**

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[Abstract]   [PDF Full-Text (338 KB)]   **IEEE CNF**

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1 **A low power 1 Mbit MRAM based on 1T1MTJ bit cell integrated with copper interconnects**
Durlam, M.; Naji, P.; Omair, A.; DeHerrera, M.; Calder, J.; Slaughter, J.M.; Engel, B.; Rizzo, N.; Grynkewich, G.; Butcher, B.; Tracy, C.; Smith, K.; Kyler, K.; Ren, J.; Molla, J.; Feil, B.; Williams, R.; Tehrani, S.;
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[Abstract]   [PDF Full-Text (396 KB)]   **IEEE CNF**

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2 **A 1-Mbit MRAM based on 1T1MTJ bit cell integrated with copper interconnects**
Durlam, M.; Naji, P.J.; Omair, A.; DeHerrera, M.; Calder, J.; Slaughter, J.M.; Engel, B.N.; Rizzo, N.D.; Grynkewich, G.; Butcher, B.; Tracy, C.; Smith, K.; Kyler, K.W.; Ren, J.J.; Molla, J.A.; Feil, W.A.; Williams, R.G.; Tehrani, S.;
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Durlam, M.; Addie, D.; Akerman, J.; Butcher, B.; Brown, P.; Chan, J.; DeHerrera, M.; Engel, B.N.; Feil, B.; Grynkewich, G.; Janesky, J.; Johnson, M.; Kyler, K.; Molla, J.; Martin, J.; Nagel, K.; Ren, J.; Rizzo, N.D.; Rodriguez, T.; Savtchenko, L.; Salter, J.; Slaughter, J.M.; Smith, K.; Sun, J.J.; Lien, M.; Papworth, K.; Shah, P.; Qin, W.; Williams, R.; Wise, L.; Tehrani, S.;
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Jeong, H.S.; Jeong, G.T.; Koh, G.H.; Song, I.H.; Park, W.J.; Kim, T.W.; Jeong, S.J.; Hwang, Y.N.; Ahn, S.J.; Kim, H.J.; Hong, J.S.; Jeong, W.C.; Lee, S.H.; Park, J.H.; Cho, W.Y.; Kim, J.S.; Song, S.H.; Park, S.O.; Jeong, U.I.; Kim, K.;
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Au, E.K.S.; Ki, W.-H.; Mow, W.H.; Hung, S.T.; Wong, C.Y.;
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Dexin Wang; Daughton, J.M.; Reed, D.; Wang, W.D.; Jian-Qing Wang;
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11 A novel low power VMRAM/MTJ design with robust magnetic switching
Xiaochun Zhu; Jian-Gang Zhu;
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Application Filing Date - AD (1):
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Detailed Description Text - DETX (13):
   The two commonpower-save modes are referred to as IDLE2 and STANDBY. In the
IDLE2 mode, the actuator 63 is parked (i.e., moved to the outer edge of disk
60), and the servo control electronics 53 and read electronics, including
preamplifier and channel 54, are turned off. The IDLE2 mode thus substantially
reduces or removes power to servo control electronics 53, preamplifier and
channel 54. In the IDLE2 power-save mode, it is also possible to reduce **power
to controller** 56 because the servo and **read** tasks are not active. In the
STANDBY mode, the actuator 63 is moved to its parking location and the spindle
motor 62 and spindle driver 51 are turned off. The STANDBY power-save mode has
all the power savings of IDLE2, plus the additional reduction in power to
spindle control electronics portion of controller 56 and spindle driver 51. In
some implementations, buffer 57 may also be turned off in one or both of the
IDLE2 and STANDBY modes. Additional power-save modes are also possible. For
example, the **SLEEP mode** includes the power-save features of STANDBY and also
has almost all remaining electronics turned off, leaving powered on only a
portion of the controller 56 and whatever else is necessary to respond to a
SLEEP recovery command from computer IDE controller 6 (FIG. 1).